



EXPRESS MAIL NO. EV170139888US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Salvatore Leonardi
Application No. : 09/853.833
Filed : May 10, 2001
For : INTEGRATED DEVICE WITH A TRENCH ISOLATION
STRUCTURE, AND FABRICATION PROCESS THEREFOR

RECEIVED
SEP 14 2002
TECHNOLOGY CENTER 2000

Examiner : Leonardo Andujar
Art Unit : 2826
Docket No. : 856063.694
Date : September 30, 2002

Commissioner for Patents
Washington, DC 20231

AMENDMENT

Commissioner for Patents:

In response to the Office Action dated May 31, 2002, please extend the period of time for response one month, to expire on September 30, 2002. Enclosed are a Petition for an Extension of Time and the requisite fee. Applicant respectfully requests that the above-identified application be amended as follows:

In the Title:

Title has been amended to read as follows:

ISOLATION TRENCH STRUCTURE FOR INTEGRATED DEVICES

In the Specification:

Paragraph beginning at line 1 of page 3 has been amended as follows:

The process sequence that leads to an integrated device IC being defined with junction isolation wells IS, starts with a semiconductor material 1 of the N⁺ type and comprises